

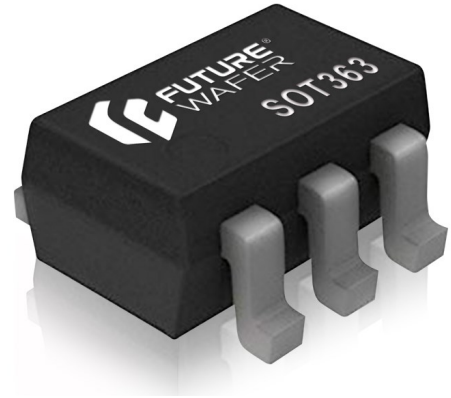
## 1. Product profile

### 1-1.General description

The UMH13NTH of digital transistors is designed to replace a single device and its external resistor bias network. The bias resistor transistor contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base– emitter resistor. The UMH eliminates these individual components by integrating them into a single device. The use of a UMH can reduce both system cost and board space.

### 1-2. Features

- $BV_{CC} > 50V$
- $I_C = 100mA$  high collector current
- Built-In bias resistors  $R_1 = 4.7K\Omega, R_2 = 47K\Omega$
- Pair of PNP transistors that are intrinsically matched
- Simplifies circuit design
- Reduce board space
- Reduce component count



SOT363

### 1-3. Applications

- Inverter
- Interface
- Driver

### 1-4. Mechanical characteristics

- Molded SOT-363 package
- Packing:tape and ree
- Flammability rating UL 94V-0
- Halogen free
- Moisture sensitivity levels (MSL): Level 1



## 2. Maximum ratings

Table 1. maximum ratings

Parameter	Symbol	Value	Units
Supply voltage	V <sub>CC</sub>	50	V
Input voltage	V <sub>IN</sub>	-10 to 40	
Output current	I <sub>O</sub>	100	mA
Max. collector current	I <sub>C</sub>		
Total power dissipation	P <sub>tot</sub>	150	mW
Junction temperature	T <sub>j</sub>	-55 to +150	°C
Storage temperature range	T <sub>stg</sub>	-55 to +150	

## 3. Electrical characteristics

Table 3. Electrical characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
Input voltage	V <sub>IN-OFF</sub>	V <sub>CC</sub> = 5V , I <sub>O</sub> = 100uA	0.5	-	-	V
	V <sub>IN-ON</sub>	V <sub>O</sub> = 0.3V , I <sub>O</sub> = 5mA	-	-	1.3	
Output voltage	V <sub>O-ON</sub>	I <sub>O</sub> = 10mA , I <sub>IN</sub> = 0.5mA	-	-	0.3	
Input current	I <sub>IN</sub>	V <sub>IN</sub> = 5.0V	-	-	1.8	mA
Output current	I <sub>O(OFF)</sub>	V <sub>CC</sub> = 50Vdc, V <sub>I</sub> = 0V	-	-	0.5	uA

## 4. On characteristics

Table 5. On characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
DC current gain	h <sub>FE</sub>	I <sub>C</sub> = 10mA, V <sub>O</sub> = 5V	80	-	-	-
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	KΩ
Resistance ratio	R <sub>2</sub> / R <sub>1</sub>	-	8	10	12	-

## 5. Small signal characteristics

Table 6. small signal characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
Current gain bandwidth product	f <sub>T</sub>	I <sub>O</sub> = 5mA, V <sub>CE</sub> = 10V, f=100MHz	-	250	-	MHz
Collector capacitance	C <sub>C</sub>	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz	-	-	2.5	pF

### 6. Rating and characteristics curve

Fig. 1 Input voltage (off) characteristics

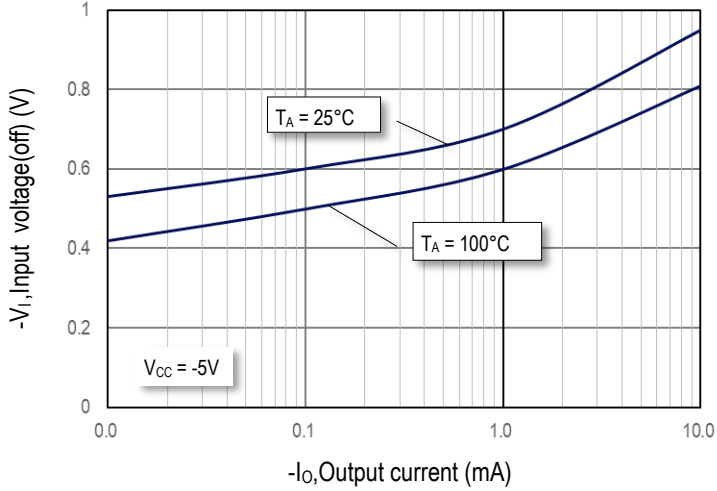


Fig. 2 Output voltage characteristics

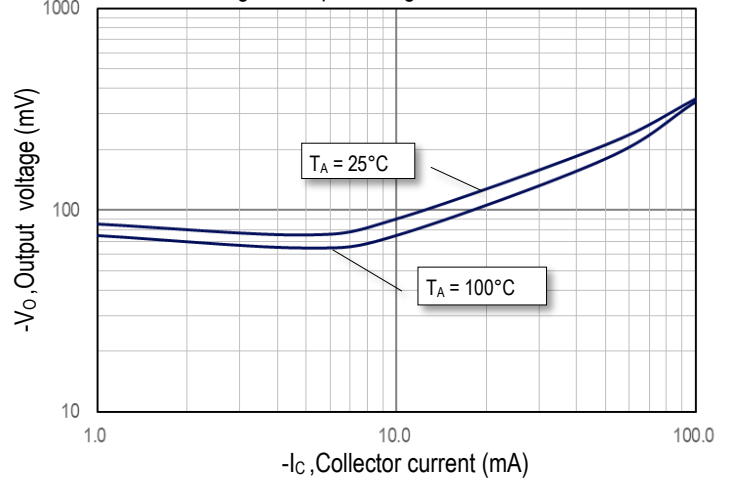


Fig. 3 Power derating curve

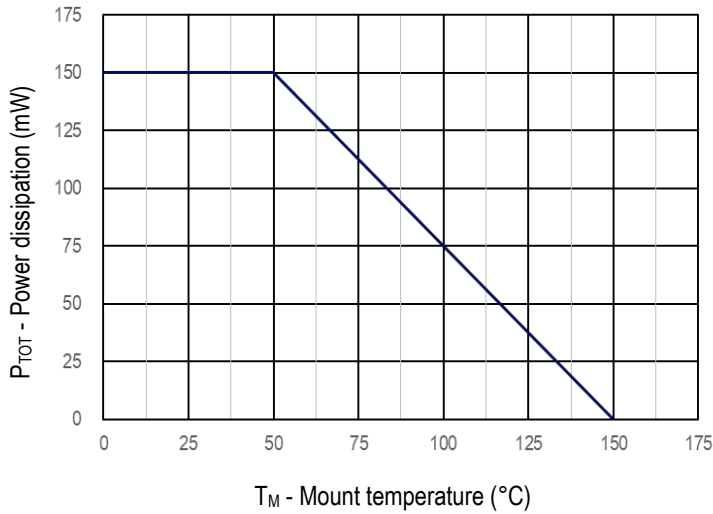


Fig. 4 DC Current gain vs output current

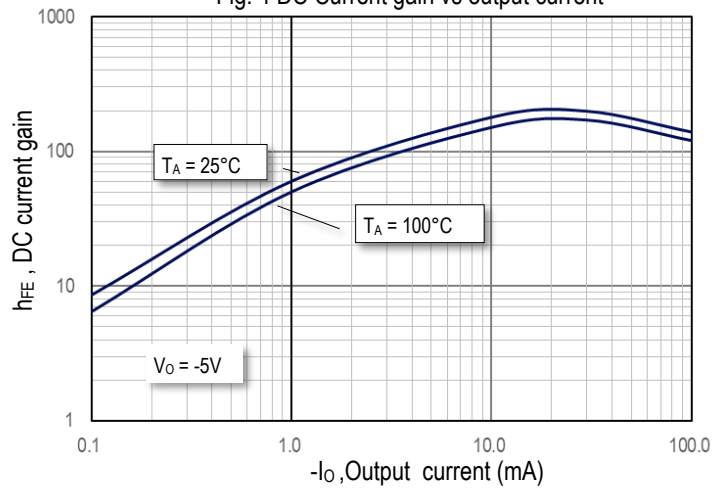


Fig. 5 Input voltage vs output current (On) characteristics

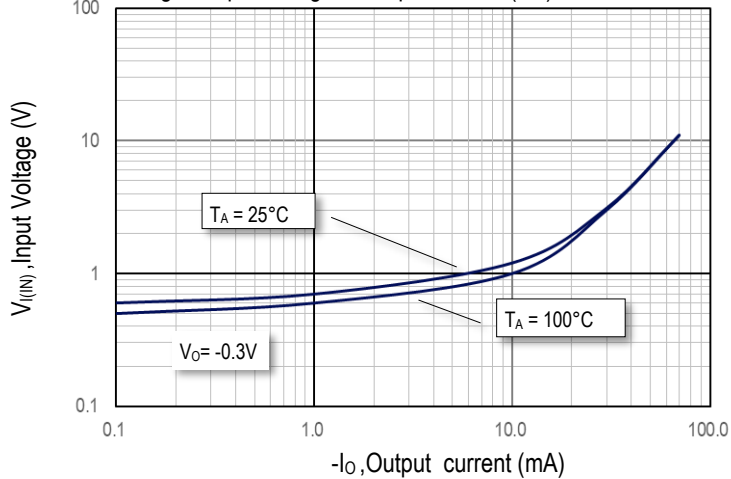
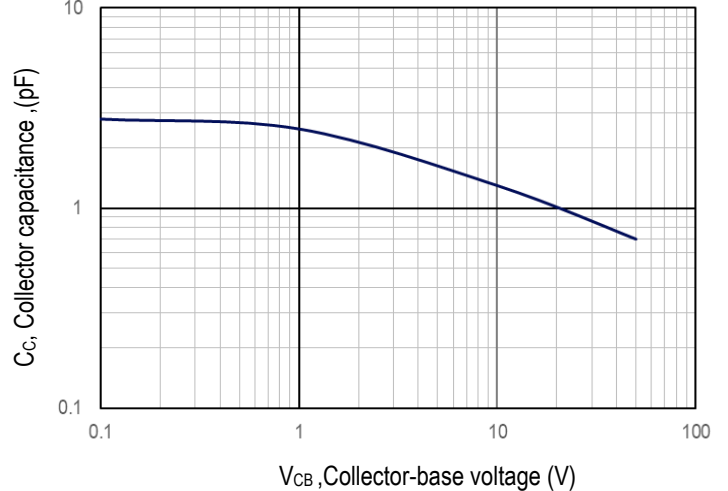


Fig. 6 Collector capacitance of collector-base voltage



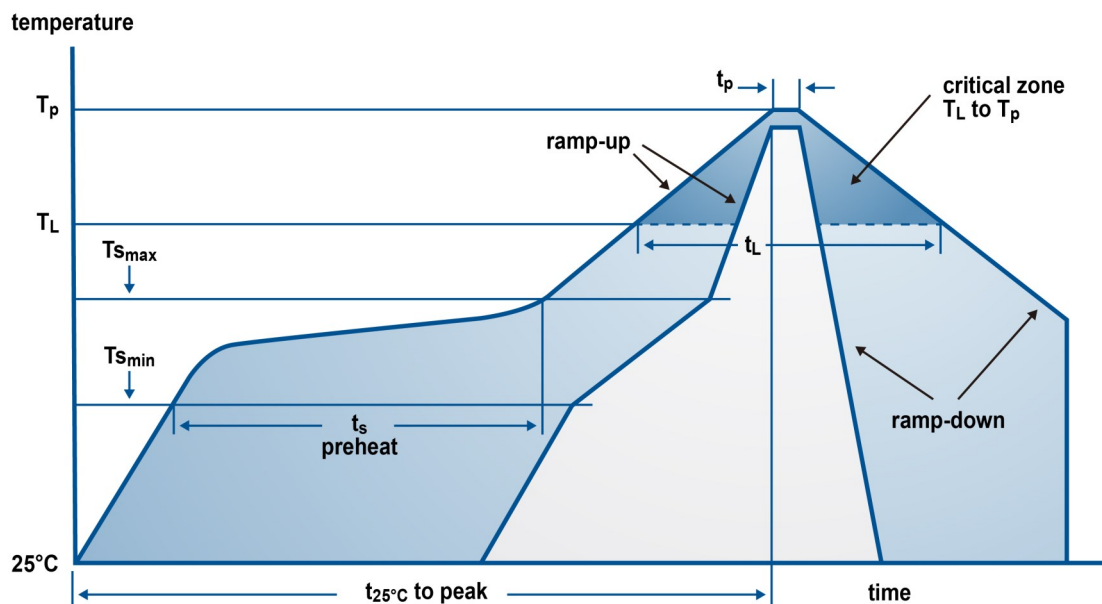
## 7. Recommended reflow soldering profile

### 7-1. Limiting value

The below temperature profile for moisture sensitivity characterization is based on the IPC/JEDEC joint industry standard: J-STD-020D-01.

Profile Feature	SnPb eutectic assembly	Pb-free assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3 °C/s maximum	3 °C/s maximum
<b>Preheat</b>		
Temperature minimum (T <sub>smin</sub> )	100 °C	150 °C
Temperature maximum (T <sub>smax</sub> )	150 °C	200 °C
Time (t <sub>smin</sub> to t <sub>smax</sub> )	60 s to 120 s	60 s to 180 s
<b>Time maintained above</b>		
Temperature (T <sub>L</sub> )	183 °C	217 °C
Time (t <sub>L</sub> )	60 s to 150 s	60 s to 150 s
<b>Peak/classification temperature (T)</b>	235 °C	260 °C
<b>Number of allowed reflow cycles</b>	3	3
Time within 5 °C of actual peak temperature (t <sub>p</sub> )	10 s to 30 s	20 s to 40 s
<b>Ramp-down rate</b>	6 °C/s maximum	6 °C/s maximum
Time 25 °C to peak temperature	6 minutes maximum	8 minutes maximum

### 7-2. Reflow soldering profile



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## 8. Package information

### 8-1. Dimension

Plastic surface-mounted package ; 6 Leads

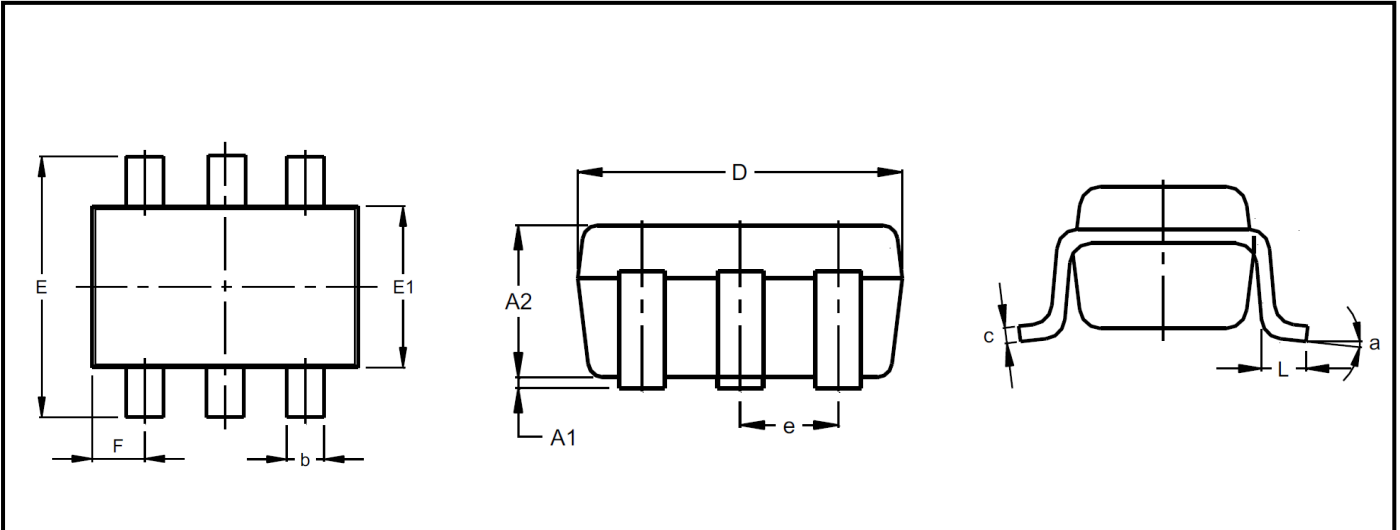


Table 7. Package summary

Dimension in mm

Dim	A1	A2	b	c	D	E	E1	e	F	L	a
Min	0.00	0.9	0.10	0.10	1.8	2.0	1.15	-	0.40	0.20	-
Typ	-	-	-	-	-	-	-	0.65	-	-	-
Max	0.10	1.0	0.30	0.22	2.2	2.2	1.35	-	0.45	0.40	8°

### 8-2. PCB Pad layout recommendation

Reflow soldering footprint for SOT-363

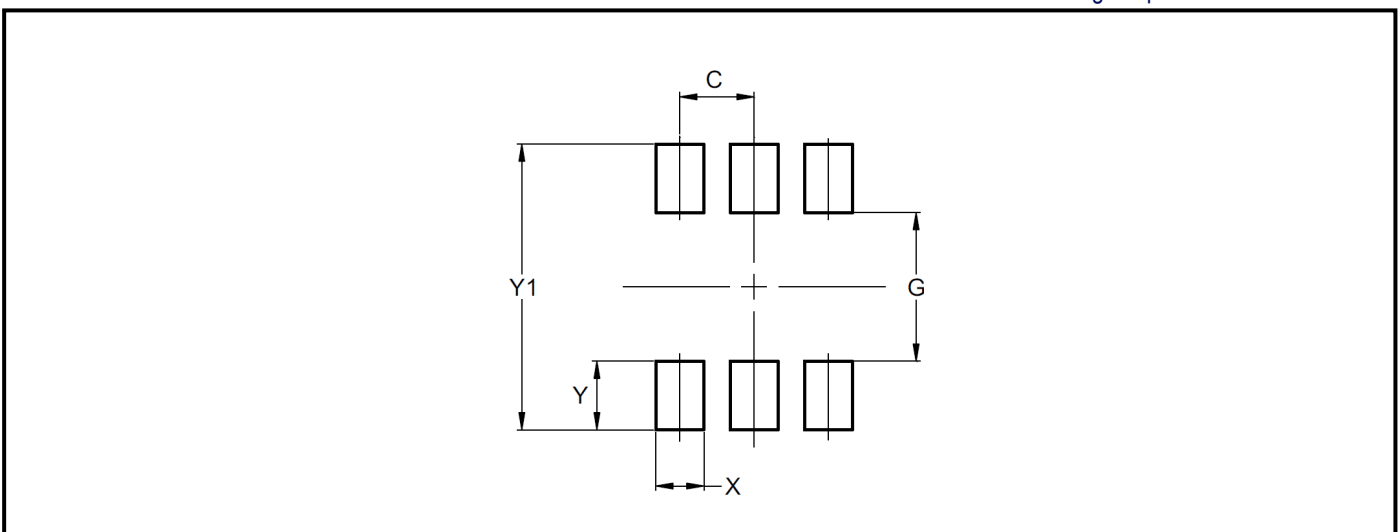


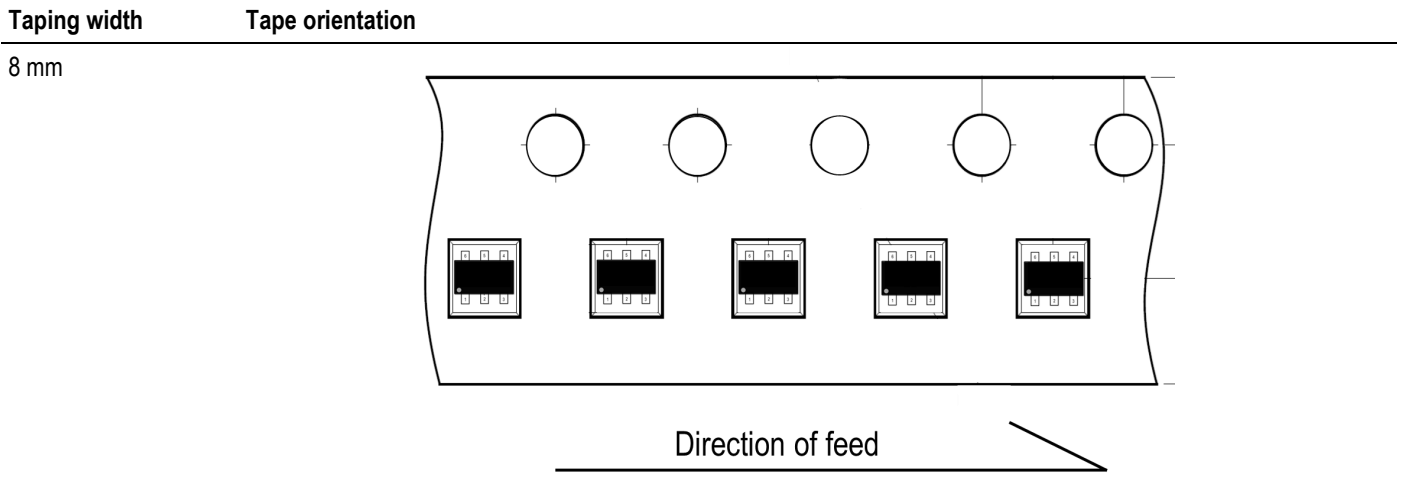
Table 8. layout summary

Dimension in mm

Dim	C	G	X	Y	Y1
Value	0.65	1.30	0.42	0.60	2.50

## 9. Packing

### 9-1. Taping and reel specification



### 9-2. Embossed carrier tape specification

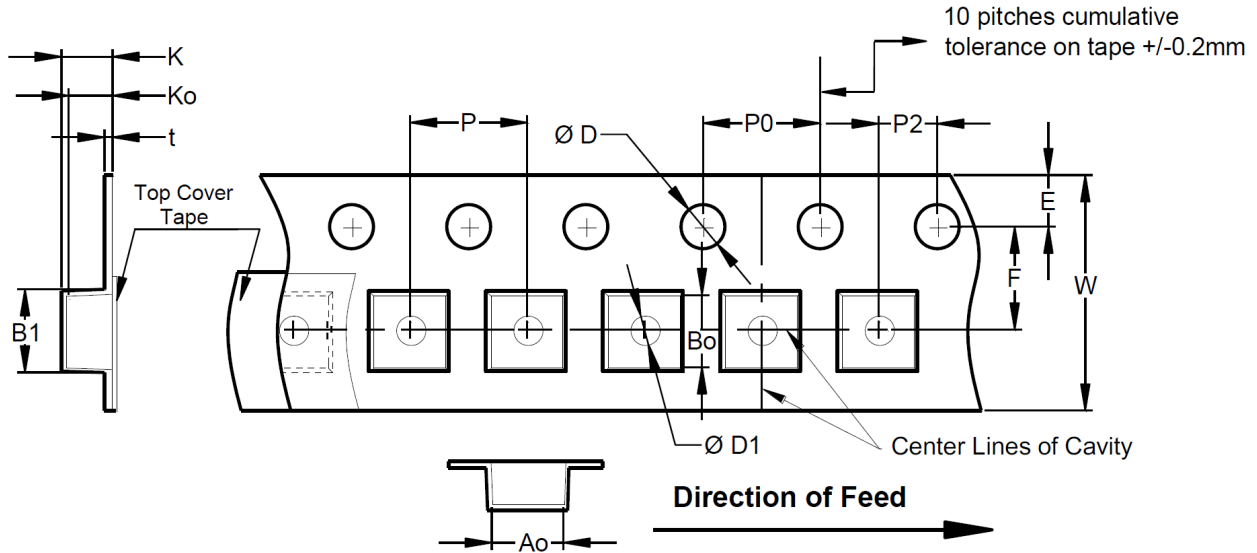


Table 9. tape summary

Dimension in mm

Dimension	W	B1	D	D1	E	F	K	P	P0	P2	t	W
Value	8 mm	4.5 Max	1.5+ 0.1/-0.0	0.35 Min.	1.75 ±0.10	3.5 ±0.05	2.4 Max.	4.0 ±0.10	4.0 ±0.10	2.0 ±0.05	0.4 Max.	8 ±0.3
A0 / B0 /	Determined by component size. The clearance between the component and the cavity must comply to the rotational and lateral movement requirement provided in figures in the "maximum component movement in tape pocket" section.											

## 10. Surface mount reel specification

### 10-1. Reel specification

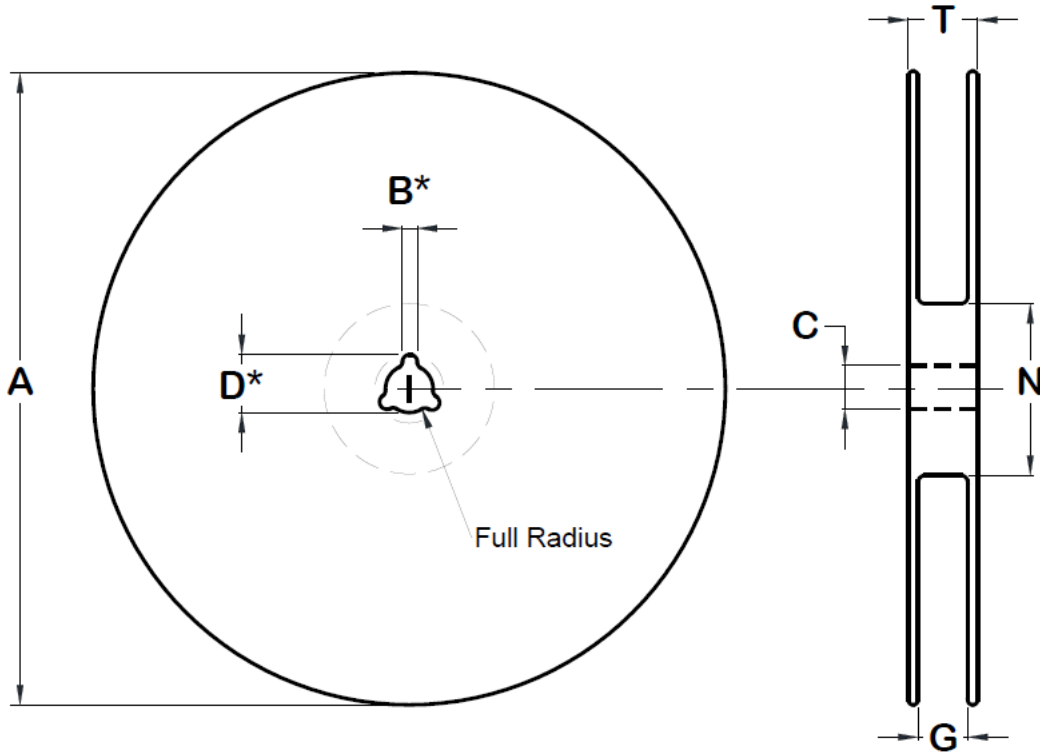
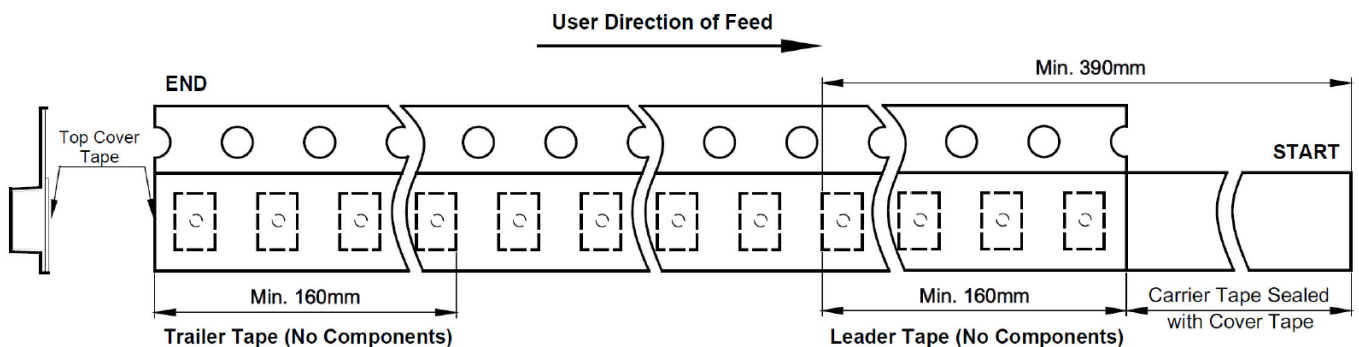


Table 10. Reel information

Unit : mm

Dimension	Tape width	Reel size	A	B	C	D	N	G	T
Value	8 mm	7"	178	2.0	13	20.5	55	8.4	14.4
			±2	+0.5-0	+0.5-0.2	±0.2	±5	+1.5/ -0.0	Max.

### 10-2. Tape leader and trailer specification



## 11. Ordering information

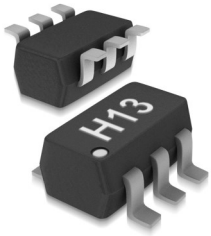
Table 11. Ordering information

Part number	Package name	Description	Packing
UMH13NTH	SOT-363	Plastic surface-mounted package; 6 leads	3,000 Pcs / 7" tape and reel

## 12. Marking information


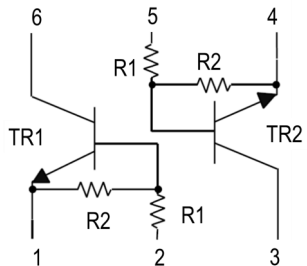
Table 12. Marking codes

Part number	Marking code
UMH13NTH	H13



## 13. Pinning information

Table 13. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E 1	Emitter 1		
2	B 1	Base 1		
3	C 2	Collector 2		
4	E 2	Emitter 2		
5	B 2	Base 2		
6	C 1	Collector 1		



**50V , R1 = 4.7 kΩ , R2 = 47 kΩ  
NPN / NPN Digital Transistors****14. Legal information****Definitions**

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**15. Revision history**

Table 14. Revision history

<b>Version</b>	<b>Document ID</b>	<b>Release date</b>	<b>Change notice</b>	<b>Basis</b>
A	F51830W	03-Aug-2018	New develop	Market
4.0		24-Mar-2023	Update version	System